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A schematic diagram of a 16-bit parallel adder. It consists of 16 full adders connected in series. A carry-in input 'C' is shown at the top left, entering the first full adder. The carry-out of the final full adder is labeled 'ON' at the bottom right. A hand icon points to the output of the 16th full adder. A curved arrow on the right indicates the direction of the carry propagation.

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